



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al. Art Unit : 2826
Serial No. : 10/076,154 Examiner : Tan N. Tran
Filed : February 14, 2002
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
Washington, D.C. 20231

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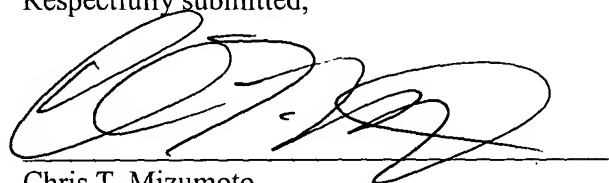
RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed October 8, 2002, applicant elects the invention of Group III (claims 18 to 23) drawn to a semiconductor device. Claim 1 is deemed to be generic and therefore should be included in Group III. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 11/6/02


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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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